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45209 7590 08/06/2008

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<i>In Chong H</i>	(Depositor's name)
<i>1/6/08</i>	(Signature)
<i>1/6/08</i>	(Date)

APPLICATION NO.	FILED DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/815,903	03/31/2004	Debendra Das Sharma	42P18579	9298

TITLE OF INVENTION: STRATEGY TO VERIFY ASYNCHRONOUS LINKS ACROSS CHIPS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	11/06/2008

EXAMINER	ART UNIT	CLASS-SUBCLASS
BAE, JI H	2115	713-600000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).	2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.	1. Blakely, Sokoloff, Taylor & Zafman LLP
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(A) NAME OF ASSIGNEE

Intel Corporation

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:

- Issue Fee
 Publication Fee (No small entity discount permitted)
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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
 b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Edwin H. Taylor

Date

1/6/08

Registration No.

25,129

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